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P.01/04

OCT 2 1 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application Serial No	
Filing Date	luly 7, 2003
Inventor	Kunal R Parekh et al
Assignee	Micron Technology Inc
Group Art Unit	2845
Examiner	H lev Tea
Attorney Docket No	MI22-2280
Customer No.	021567
Title	Methods of Forming Memory Circuitry

Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450

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- 2. Transmittal Form
- Statement of the Substance of the Interview

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		Application Number	10/615,287	erion unies	s ir displays a valid OMB control number.	
TRANSMITTAL		Filing Date	July 7, 2003	July 7, 2003		
	FORM	First Named Inventor	Kunal R. Parel	kh		
		Art Unit	2812			
(to be used for all	correspondence after initial filing)	Examiner Name	H. Jey Teal			
Total Number of Pa	ages in This Submission	Attorney Docket Number	MI22-2260			
ENCLOSURES (Check all that apply)						
Fee Transmi	ittal Form D	Práwing(s)		Afte	r Allowance Communication to TC	
Fee /	Attached Li	Icensing-related Papers		App of A	eal Communication to Board opeals and Interferences	
Affida Extension of Express Aba Information D Certified Cop Document(s) Reply to Miss Incomplete A Reply	Final avits/declaration(s) Time Request indonment Request Disclosure Statement Dy of Priority Remark: Customer NO FEE R	Petition Petition to Convert to a Provisional Application Ower of Attorney, Revocation change of Correspondence Ar erminal Disclaimer request for Refund D, Number of CD(s) Landscape Table on CD S No. 021567. REQUIRED. a fee, charge to Deposit Accord	ddress	App (App Prop State Other belo Certificate Statement of 10/19/0	eal Communication to TC eal Notice, Brief, Reply Brief) inetary Information us Letter er Enclosure(s) (please identify w); of Facsimile Transmission; of the Substance of the Interview	
	SIGNATURE OF	APPLICANT, ATTOR	NEY, OR A	GENT		
Firm Name	Vells St. John P.S.					
Signature New York						
Printed name Mark S. Matkin						
Date	Date /d/21/04 Reg. No. 32,268					
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WELLS ST JOHN PS

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No	
Filing Date	July 7, 2003
Inventor	Kunal R. Parekh et al.
Assignee	Micron Technology, Inc.
Group Art Unit	2812
Examiner	H. Jey Tsai
Attorney's Docket No	
Customer No	
Title	Methods of Forming Memory Circuitry

STATEMENT OF THE SUBSTANCE OF THE INTERVIEW

To:

Commissioner for Patents

ATTENTION: Examiner H. Jey Tsai Group Art Unit 2812

P. O. Box 1450

Alexandria, VA 22313-1450

VIA FACSIMILE

From:

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An interview was conducted between the undersigned and Examiner Tsai on October 19, 2004.

The undersigned emphasized the arguments and amendments presented in the last-filed Amendment. Specifically, it was asserted that amended claim 1 was not anticipated by U.S. Patent No. 6,177,695 Jeng since Jeng's material 61 is not received at any common elevation with conductive portions of Jeng's word lines. The Examiner concurred with this assertion in looking at Jeng's figures only, but wanted to review the Jeng specification in more detail in this regard.

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WELLS ST JOHN PS

With respect to claim 13, the undersigned asserted the significance of sacrificial plugging material being removed <u>after</u> formation of the bit line, and that Jeng discloses the opposite, namely removal of material 81 before the bit line formation of Fig. 2k. The Examiner agreed to further consider this issue. The undersigned also noted that in Applicant's last-filed response, the page 22, last paragraph, first line, reference to claim 33 was intended to be to claim 13.

Claims 23 and 35 were also discussed. The Examiner agreed that such claims were patentable over the art of record.

Respectfully submitted,

Dated: <u>/0-2/-</u>

Mark S. Matkin Reg. No. 32,268

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